

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of : Atty. Docket: 00-CCL-096  
Yves MORAND et al. : Examiner: LEE, Hsien Ming  
Application No. 09/932,513 : Group Art Unit: 2823  
Filed: August 17, 2001 :  
For: *PROCESS FOR FABRICATING A CAPACITOR WITHIN AN  
INTEGRATED CIRCUIT, AND CORRESPONDING INTEGRATED  
CIRCUIT*

**RESPONSE WITH AMENDMENT UNDER 37 C.F.R. § 1.111**

**VIA FACSIMILE (703) 872-9318**

Commissioner for Patents

P.O. Box 1450

Alexandria VA 22313-1450

**Attention: Examiner Hsien Ming Lee**

SIR:

The following amendment is being submitted in response to the Office Action dated May 14, 2003, the due date for response to which has been extended to October 14, 2003 by the attached Petition for Extension of Time in connection with the above-identified application. Please enter and consider the following amendment and remarks as follows:

I hereby certify that this correspondence is being facsimile transmitted to the  
United States Patent and Trademark Office, on

By: Applicant, Assignee, or Representative

Signature: \_\_\_\_\_

10/14/03  
JOSE GUTMAN  
*Jose Gutman*